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APPLICATION NO	. F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,473	473 07/11/2003		Xiaowei Deng	TI-33969	5307
23494	7590	01/03/2006		EXAMINER	
TEXAS II	NSTRUM	ENTS INCORPOR	NGUYEN, VAN THU T		
P O BOX 655474, M/S 3999 DALLAS, TX 75265				ART UNIT	PAPER NUMBER
,				2824	
			•	DATE MAILED: 01/03/200	6 ·

Please find below and/or attached an Office communication concerning this application or proceeding.

			Α	£			
		Application No.	Applicant(s)				
		10/618,473	DENG ET AL.				
Office Action Summary		Examiner	Art Unit				
		VanThu Nguyen	2824				
Period f	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the	correspondence address				
WHIC - Exte after - If NC - Fails Any	HORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DAPAISIONS of time may be available under the provisions of 37 CFR 1.13 or SIX (6) MONTHS from the mailing date of this communication. Or period for reply is specified above, the maximum statutory period we ure to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATIO 36(a). In no event, however, may a reply be ti vill apply and will expire SIX (6) MONTHS fror , cause the application to become ABANDON	DN. imely filed m the mailing date of this communication. ED (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on <u>05 De</u>	<u>ecember 2005</u> .					
2a)⊠	This action is FINAL . 2b) This action is non-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	153 O.G. 213.				
Disposit	tion of Claims						
4)⊠	☑ Claim(s) <u>1-9 and 11-25</u> is/are pending in the application.						
	4a) Of the above claim(s) 19-22 is/are withdrawn from consideration.						
5)⊠	Claim(s) <u>1-4,6,9 and 23</u> is/are allowed.						
6)⊠	☑ Claim(s) <u>11-12, 14, 16-18, 24</u> is/are rejected.						
7)⊠	Claim(s) 13, 15 and 25 is/are objected to.						
8)□	Claim(s) are subject to restriction and/or	r election requirement.					
Applicat	tion Papers						
9)🖂	The specification is objected to by the Examine	r.					
10)🖂	The drawing(s) filed on <u>05 December 2005</u> is/a	re: a)⊠ accepted or b)□ object	ated to by the Examiner.				
	Applicant may not request that any objection to the	drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the correct	ion is required if the drawing(s) is ol	bjected to. See 37 CFR 1.121(d).				
11)	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	e Action or form PTO-152.				
Priority :	under 35 U.S.C. § 119						
-	Acknowledgment is made of a claim for foreign ☐ All b)☐ Some * c)☐ None of:	priority under 35 U.S.C. § 119(a	a)-(d) or (f).				
	1. Certified copies of the priority documents						
	2. Certified copies of the priority documents						
	3. Copies of the certified copies of the prior	_ -	ed in this National Stage				
* (application from the International Bureau	• • • • • • • • • • • • • • • • • • • •					
	See the attached detailed Office action for a list	of the certified copies not receiv	ea.				
Attachmen	nt(e)						
	n(s) ce of References Cited (PTO-892)	4) Interview Summar	v (PTO-413)				
2) 🔲 Notic	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D	Date				
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	6) Other:	Patent Application (PTO-152)				

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Response to Amendment

1. This Office Action is in response to Amendment filed on December 5, 2005.

- 2. Claims 1-9, 11-18, 23-25 are present for examination.
- 3. Claim 25 is newly added.
- 4. Claim 10 was previously cancelled.
- 5. Claims 19-22 are withdrawn from further consideration.
- 6. Drawings filed on December 5, 2005 are accepted by Examiner.

Response to Arguments

7. Applicants' arguments filed December 5, 2005 have been fully considered but they are not persuasive. Amended limitation in claim 11 can be understood as a power switch having a control signal input [gate], which is the control terminal of any transistor.

Specification

8. The disclosure is objected to because of the following informalities. Appropriate correction is required.

Brief description of FIG. 4 is required under "BRIEF DESCRIPTION OF THE DRAWINGS" section in Specification.

Claim Rejections - 35 USC § 102

9. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

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10. Claims 11-12, 14, 16-18, 24 are rejected under 35 U.S.C. 102(e) as being anticipated by Yanagisawa et al. (U.S. Patent No. 2001/0028581).

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Regarding claim 11, Yanagisawa discloses, in FIG. 16(B) a semiconductor device comprising:

a row of memory cells (inherently connected to word line lw[3] for example);
a control circuitry preceding the row of memory cells (the lower left NOR gate);
and

an intervention circuit (n-channel transistor connected to signal a), instantiated within the control circuitry proximal to the row of memory cells, adapted to hold the row of memory cells at a desired state while control circuitry preceding the intervention circuit is powered down with a power switch having a control signal input (via p-channel transistor having a control signal input [gate] connected to signal a).

Regarding claims 12, 14, 16-18, 24, Yanagisawa also discloses a wordline lw[3] and a driver circuit comprising p-channel and n-channel transistors coupled to signal b (with or without p-channel transistor coupled to signal a) (as in claim 12); the intervention circuit is a n-channel transistor (as in claim 14); the intervention circuit is instantiated between the wordline and driver circuitry (as in claim 16); the intervention circuit is coupled to a first assertion signal source (signal a) that is also coupled to the driver circuitry (as in claim 17); intervention circuit is coupled to a first assertion signal source (signal a), and driver circuit coupled to second assertion signal source (signal b) (as in claim 18); pre-driver circuit [for generating global signal, e.g. gwb[0]] (as in claim 24).

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Allowable Subject Matter

11. Claims 1-4, 6, 9 and 23 are allowed.

12. Claims 13, 15 and 25 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VanThu Nguyen whose telephone number is (571) 272-1881. The examiner can normally be reached on Monday-Friday, 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

December 29, 2005

VanThu Nguyen
Primary Examiner
Art Unit 2824